Challenge: Capturing the Promises of Native 400GE Products

Using 100 gigabit Ethernet (GE) technologies to get 400GE speeds is a costly and complex method to get higher bandwidth in the datacenter. The native 400GE hardware now coming to market will bring better economies of scale, denser configuration, and more attractive price-per-port.

For large-scale data center networks, 400GE promises four times the bandwidth of 100GE in equivalent or less rack space. These critical factors save costs in operations. When it comes to testing new 400GE equipment, the same is true using Ixia’s AresONE-400GE test products.

Solution: Density with Performance—Not One or the Other

Ixia’s AresONE 400GE product family is a compact high-port-density 400GE test solution for accelerating performance and benchmark testing of your high density 3.23-12.8Tbps networking devices. AresONE offers a factory and a field-upgrade that provides 2x200GE, 4x100GE, and 8x50GE speed test capabilities. This provides your development teams the speeds and test options they need to create the networking technologies of the future. AresONE OSFP fixed chassis are available in two models:

- Full-feature performance and scale
- Reduced-feature performance and scale

Both models enable full line-rate traffic generation functionality for transmit, receive, and capture. This facilitates RFC benchmark testing, stress testing, and hardware/ASIC bring-up in high-port-count test beds. It can be used for optics and cable qualification, interoperability, and functional test. Plus, with IxNetwork, Ixia offers the broadest and highest performance Layer 2/3 routing protocol emulation coverage and performance available in the industry.

Highlights

- Save power, cooling, and rack space with the Ixia’s compact 8-port 400GE OSFP L1-3 test solution
- Validate high port count devices for performance, scalability, and interoperability with AresONE multi-speed 400, 200, 100, 50GE test capabilities and Ixia’s IxNetwork Layer 2/3 test application
- Get faster test feature development and response times as industry standards evolve because of Ixia-developed intellectual property for the critical elements of 400GE: MAC, PCS, FEC, FEC symbol error distribution, and FEC error injection and statistics
- Speed hardware development and bring-up with Layer 1 BERT and PCS lanes Tx/Rx test capability with IxSuiteStore
- Rely on a proven test solution for validating mission-critical network infrastructure—AresONE OSFP extends Ixia’s proven AresONE QSFP-DD 400GE test solutions
**AresONE, T400GD-8P-OSFP and T400GD-4P-OSFP:** Full performance, 400GE 8-port and 4-port models in a 2RU fixed chassis system designed for high density full-scale and performance enterprise and data center switch and router testing.

**AresONE, T400GDR-8P-OSFP and T400GDR-4P-OSFP:** Reduced performance 400GE 8-port and 4-port models in a 2RU fixed chassis system designed for high-density hardware, ASIC, cable/optics qualification, RFC benchmark, and interoperability testing for high-port-count testing. The OSFP-R400GE scales down the L2/3 feature set and L2/3 networking protocol scaling, without compromising routing protocol coverage, while increasing affordability.

**PAY AS YOU GROW—FULL AND REDUCED, 4-PORT AND 8-PORT VARIANTS, ALL FIELD UPGRADEABLE**

The ability to upgrade AresONE fixed chassis to have AresONE grow with your needs is un-paralleled. Any AresONE 8-port or 4-port reduced model can be upgraded in the field to a full performance, higher-capacity, L2/3 feature set and L2/3 networking protocol scaling performance model. Any 4-port model can be upgraded to an 8-port model. Mix and match whatever upgrade or upgrades that you require. No longer are you stuck with a dedicated piece of hardware with no hope of extending its capabilities. Let AresONE assist you in the critical ROI analysis. AresONE upgrades extend the reuse of the fixed chassis system and improves your ROI. This is a unique capability that AresONE QSFP-DD and OSFP models offer.

**KEY FEATURES**

- Line-rate 400Gbps packet generation, capture, and analysis of received traffic to detect and debug data transmission errors for multiple speeds, including 2x200GE, 4x100GE and 8x50GE
- Multi-rate speed option that includes 2x200GE, 4x100GE, and 8x50GE speed modes and fan-out support; these speed modes are compliant with IEEE 802.3cd specification
- New IxNetwork protocol bundles that provide easy and flexible pricing designed for fixed chassis systems
- IxSuiteStore, the industry’s first fully automated IEEE 802.3bs-based test suite that enables automated validation of 400GE implementations, includes testing of physical coding sublayer (PCS) lanes, BER, KP4 FEC bit-error distribution with error insertion and link stability
- All reduced and 4-port models may be upgraded via one or more options to an 8-port full-performance mode
- Line-rate, at all speeds with per-port and per-flow statistics
- High-latency measurement resolution at 0.625ns at the 400GE speed and 1.25ns at 200GE
- RS-544 (KP4) Forward Error Correction (FEC) support for all speeds (400/200/100/50GE)
- An excellent test platform for full line-rate 400/200/100/50 Gb/s to evaluate 400GE ASIC designs, FPGAs, and hardware switch fabrics that use the new 8x56Gb/s electrical interface with PAM4 encoding that is IEEE 802.3bs and IEEE 802.3cd compliant
- Auto-negotiation and link training support
- 400GE and 2x200GE FEC symbol error injection and FEC symbol error distribution analysis with a comprehensive set of FEC corrected and uncorrected count and rate statistics; Bit Error Rate (BER) statistics for pre- and post-FEC analysis are provided
- Ixia instrumentation including floating timestamp, sequence number and flow identification, and data integrity
• 400G PCS lanes Transmit, error injection testing and receive measurement:
  o Per-lane controls and status, FEC and error monitoring, error insertion, lane mapping and skew insertion; see Specification Table in this datasheet for details, as capabilities may vary per Ethernet speed
• Layer 1 classical BERT capability with per lane and per port BER statistics, ability to send PRBS patterns and inject bit errors per lane under user control
• +/- 100 PPM line frequency adjustment
• Inject packet errors: CRCs, runts, giants, alignments, checksum errors, and out of sequence
• Mid-to-high-range L2/3 networking protocol emulation to validate performance and scalability of L2/3 routing/switching and data center test cases using the Ixia’s IxNetwork protocol emulation application
• Supports RFC benchmarking of networking devices and equipment using industry-standard RFC benchmark tests at line-rate 400/200/100/50GE speeds
• Supported with the Native IxOS software
• Application support: backwards compatible with existing chassis and software with IxExplorer and IxNetwork
• IxExplorer, IxNetwork, and related Tcl and automation APIs

IXSUITESTORE – FAST AND EFFICIENT STANDARDS-BASED TEST METHODOLOGY FOR 400GE PAM4

The automated 400GE Transceiver and PCS Testing suite enables developers of 400GE equipment to accelerate testing and gain significant time to market advantage. Quality assurance teams can benefit from front-loading testing, flagging implementation issues more quickly, and reducing manual test time. Consumers of 400GE equipment like data center and service provider equipment validation teams can use the test suite to automate 400GE equipment and optical transceiver and copper cable validation during initial stages of qualification, to ensure quality of upgrades and avoid future interoperability issues.
The 400GE test suite is available using Ixia’s IxSuiteStore framework. The test suite validates key aspects of a 400GBASE-R PCS and supported physical media dependents (PMDs) per IEEE 802.3bs. Following are more details on this test suite.

- A set of Ixia-provided scripts exercising most of the Layer1 test capabilities of Ixia AresONE hardware
- It is also compatible with Ixia’s K400 QSFP-DD 400GE load modules
- Enables quick-start testing with basic steps and progressively guides to more advanced cases
- Customers can configure these tests to support regression testbeds
- Currently 25 tests are available, covering key validations required in a 400GE implementation
- Requires IxOS version 9.00 or later

### SPECIFICATIONS

<table>
<thead>
<tr>
<th>PRODUCT DESCRIPTION</th>
<th>T400GD-8P-OSFP</th>
<th>T400GD-4P-OSFP</th>
<th>T400GDR-8P-OSFP</th>
<th>T400GDR-4P-OSFP</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>FULL FEATURE</td>
<td>8-PORT / 4-PORT</td>
<td>REDUCED FEATURE</td>
<td>8-PORT / 4-PORT</td>
</tr>
<tr>
<td>Part Numbers</td>
<td>944-1174 / 944-1176</td>
<td></td>
<td>944-1175 / 944-1177</td>
<td></td>
</tr>
</tbody>
</table>

**Hardware Fixed Chassis System Specifications**

- **RU / Number of Ports**: 2 RU 8-port and 4-port fixed chassis systems
- **Physical Interfaces**: Native OSFP physical ports
- **Supported Port Speeds**: 400GE/port: 400GE-capable fiber and passive copper cable media 2x200, 4x100, 8x50GE capable with the purchase of a factory or a field upgrade speed option. See the Ordering Section of this datasheet.
- **CPU and Memory**: Multicore processor with 2GB of CPU memory per port
- **IEEE Interface Protocols for 400GE**: IEEE 802.3bs 200GE & 400GE, 400GBASE-R IEEE 802.3cd 50 Gb/s, 100 Gb/s, and 200 Gb/s Ethernet
## DATA SHEET

<table>
<thead>
<tr>
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<td>REDUCED FEATURE</td>
<td>8-PORT / 4-PORT</td>
</tr>
</tbody>
</table>

### Layer 1 support

400GE native ports and 200/100/50GE speed option:
- KP4 (RS-544) Ethernet Forward Error Correction, Clause 119
- Auto-negotiation and link training support
- Correctable and uncorrectable FEC statistics per-port
- FEC symbol error injection (400GE & 200GE speeds only)
- PCS lanes Tx and Rx test and statistics
- Layer 1 classical BERT

### Optical Transceiver Support

Support for all OSFP MSA 2.0 compliant optical transceivers up to Power Class 7 with 14 watts of power consumption such as: 400GBASE-DR4, 400GBASE-FR4, and 400GBASE-LR8, as well as other optical transceiver types and AOCs. Please consult the factory for specific transceiver support information.

### Copper Cable Media

400GBASE-CR8, passive, copper Direct Attached Cable (DAC) up to 3 meters in length, consult factory for longer length cable information. Active, copper DACs are supported, please consult the factory for more information. See Cables & Transceivers under the Ordering Information section of this datasheet.

### Fixed Chassis System Dimensions

- 30.9” (L) x 17.3” (W) x 3.5” (H)
- 785mm (L) x 43mm (W) x 88mm (H)

### Fixed Chassis System Weights

- ESTIMATED Hardware only: 74.6 lbs. (33.84 kg)
- ESTIMATED Shipping: 94.5 lbs. (42.86 kg) (1)

(1) Approximate (includes rackmount slides, power cords, sync cables & packaging)

### Fixed Chassis System Shipment Dimensions

ESTIMATED Packaging sizes for shipment:
- 41.5” (L) x 25.4” (W) x 14.25” (H)
- 1054mm (L) x 645mm (W) x 362mm (H)
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<th>T400GDR-8P-OSFP</th>
<th>T400GDR-4P-OSFP</th>
</tr>
</thead>
<tbody>
<tr>
<td>Full Feature</td>
<td>8-PORT / 4-PORT</td>
<td>8-PORT / 4-PORT</td>
<td>8-PORT / 4-PORT</td>
<td>8-PORT / 4-PORT</td>
</tr>
<tr>
<td>Reduced Feature</td>
<td></td>
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</tbody>
</table>

**Fixed Chassis System Electrical Power (2)**

- Operates on 100-240VAC, 50/60Hz
- 200-240VAC is single phase
- Requires (3) power sources when running 100-120VAC, 9 Amps for each power supply. AresONE fixed chassis is shipped with (3 each) 100-125VAC power cords.
- Requires (2) power sources when running 200-240VAC, 7 Amps for each power supply. For 200-240VAC power cords, order part number 942-0110 from the Ordering Section of this datasheet. The kit is provided at no charge with the purchase of a AresONE fixed chassis when 200-240VAC is required.

(2) Note: Power specifications are preliminary and are for reference for initial facility planning purposes

<table>
<thead>
<tr>
<th>Temperature (Ambient Air)</th>
<th>Operating: 41°F to 95°F (5°C to 35°C)</th>
<th>Storage: 41°F to 122°F (5°C to 50°C)</th>
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</table>

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<tr>
<th>Humidity (Ambient Air)</th>
<th>Operating: 0% to 85%, non-condensing</th>
<th>Storage: 0% to 85%, non-condensing</th>
</tr>
</thead>
</table>

|-------------------------------------|--------------------------------------------------------------------------------|

<table>
<thead>
<tr>
<th>Chassis Capacity: Maximum number of chassis and ports per model</th>
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</thead>
<tbody>
<tr>
<td><strong>T400GD-8P-OSFP (944-1174)</strong></td>
</tr>
</tbody>
</table>

- 8-port fixed chassis systems:
  - 8-port, 2RU fixed chassis with built-in star topology synchronization ports to connect up to 5 additional fixed chassis systems
  - Total single synchronized system capacity is 48-ports of 400GE in a single configuration
  - Consult factory for port count requirements beyond 48-ports in a single configuration
<table>
<thead>
<tr>
<th>PRODUCT DESCRIPTION</th>
<th>T400GD-8P-OSFP 8-PORT / 4-PORT</th>
<th>T400GD-4P-OSFP 4-PORT</th>
<th>FULL FEATURE</th>
<th>T400GDR-8P-OSFP 8-PORT / 4-PORT</th>
<th>T400GDR-4P-OSFP 4-PORT</th>
<th>REDUCED FEATURE</th>
</tr>
</thead>
<tbody>
<tr>
<td>T400GD-4P-OSFP (944-1176)</td>
<td>4-port fixed chassis systems:</td>
<td>4-port fixed chassis systems:</td>
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<td>4-port fixed chassis systems:</td>
<td>4-port fixed chassis systems:</td>
<td>4-port fixed chassis systems:</td>
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<tr>
<td>T400GDR-4P-OSFP (944-1177)</td>
<td>• 4-port, 2RU fixed chassis with built-in star topology synchronization ports to connect up to 5 additional fixed chassis systems</td>
<td>• Total single system capacity is 24-ports of 400GE in a single configuration</td>
<td>• Consult factory for port count requirements beyond 24-ports in a single configuration</td>
<td>• 4-port, 2RU fixed chassis with built-in star topology synchronization ports to connect up to 5 additional fixed chassis systems</td>
<td>• Total single system capacity is 24-ports of 400GE in a single configuration</td>
<td>• Consult factory for port count requirements beyond 24-ports in a single configuration</td>
</tr>
</tbody>
</table>

### Transmit Feature Specifications

<table>
<thead>
<tr>
<th>Transmit Engine</th>
<th>Wire-speed packet generation with timestamps, sequence numbers, data integrity, and packet group signatures</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Streams per Port and Speed (Including in Data Center Ethernet)</td>
<td>400GE: 128 2x200GE: 128 4x100GE: 32 8x50GE: 16</td>
</tr>
</tbody>
</table>
| Stream Controls | • Rate and frame size change on the fly  
• Advanced stream scheduler support |
| Minimum Frame Size | 400GE and 200GE:  
• 60 bytes at full line rate  
• 56 bytes at less than full line rate  
100GE and 50GE:  
• 64 bytes at full line rate  
• 56 bytes at less than full line rate |
| Maximum Frame Size | 16,000 bytes |
| Maximum Frame Size in Data Center Ethernet | 9,216 bytes |
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<th>T400GD-8P-OSFP T400GD-4P-OSFP FULL FEATURE 8-PORT / 4-PORT</th>
<th>T400GDR-8P-OSFP T400GDR-4P-OSFP REDUCED FEATURE 8-PORT / 4-PORT</th>
</tr>
</thead>
</table>
| Priority Flow Control | • 4 line-rate-capable queues, each supporting up to 2,500-byte frame lengths  
                                      • 1 line-rate-capable queue, non-blocking supporting up to 9,216-byte frame length | |
| Frame Length Controls | Fixed, increment by user-defined step, weighted pairs (up to 16K in 400/200/100GE and 8K in 50GE), uniform, repeatable random, IMIX, and Quad Gaussian | |
| User-Defined Fields (UDF) | Fixed, increment or decrement by user-defined step, sequence, value list, and random configurations; up to 10, 32-bit-wide UDFs are available | |
| Value Lists (Max.) per port | • 400GE: 1M / UDF  
                                      • 2x200GE: 1M / UDF  
                                      • 4x100GE: 1M / UDF  
                                      • 8x50GE: 512K / UDF | |
| Sequence (Max.) | • 400GE: 32K / UDF  
                                      • 2x200GE: 16K / UDF  
                                      • 4x100GE: 8K / UDF  
                                      • 8x50GE: 4K / UDF | |
| Error Generation (FEC and standard Ixia L2/3 Ethernet) | 400GE and 2x200GE FEC  
                                                • FEC symbol error-injection allows the user to inject FEC symbol errors using various weighted methods to achieve specific bit error rates (BER) for 400/200GE  
                                                • No FEC error insertion and related statistics for 4x100GE and 8x50GE  
                                                400GE, 2x200GE, 4x100GE, 8x50GE L2/3 Ethernet:  
                                                • Generate good CRC or force bad CRC, undersize and oversize standard Ethernet frame lengths, and bad checksum | |
<table>
<thead>
<tr>
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<th>T400GD-8P-OSFP FULL FEATURE 8-PORT / 4-PORT</th>
<th>T400GD-4P-OSFP FULL FEATURE 8-PORT / 4-PORT</th>
<th>T400GDR-8P-OSFP REDUCED FEATURE 8-PORT / 4-PORT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Physical Coding Sublayer</td>
<td>• PCS lane marker error injection</td>
<td>• PCS lane re-mapping</td>
<td>• PCS lane marker error injection</td>
</tr>
<tr>
<td></td>
<td>• PCS lane re-mapping</td>
<td>• PCS bit error generation</td>
<td></td>
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<tr>
<td>Hardware Checksum Generation</td>
<td>Checksum generation for IPv4, IP over IP, ICMP/GRE/TCP/UDP, L2TP, GTP, and multilayer checksum; support for protocol verification for control plane traffic</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Link Fault Signaling</td>
<td>• Reports, no fault, remote fault, and local fault port statistics</td>
<td>• Generate local and remote faults with controls for the number of faults and order of faults</td>
<td>• Option to have the transmit port ignore link faults from a remote link partner and send traffic anyway</td>
</tr>
<tr>
<td>Latency Measurement Resolution</td>
<td>• 400GE: 0.625 nanoseconds</td>
<td>• 2x200GE: 1.25 nanoseconds</td>
<td>• 4x100GE: 2.5ns nanoseconds</td>
</tr>
<tr>
<td></td>
<td>• 4x100GE: 2.5ns nanoseconds</td>
<td></td>
<td>• 8x50GE: 2.5ns nanoseconds</td>
</tr>
<tr>
<td>Intrinsic Latency Compensation</td>
<td>Removes inherent latency error from the port electronics for all speeds</td>
<td></td>
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</tr>
<tr>
<td>Transmit Line Clock Adjustment</td>
<td>Ability to adjust the parts-per-million (ppm) line frequency over a range of +/-100 ppm on all the ports of a 400GE fixed chassis system</td>
<td></td>
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</tr>
<tr>
<td>Transmit/Receive Loopback</td>
<td>• Internal loopback</td>
<td>• Line loopback</td>
<td></td>
</tr>
<tr>
<td>Receive Feature Specifications</td>
<td>Wire-speed packet filtering, capturing, real-time latency, and inter-arrival time for each packet group, with data integrity, and sequence checking capability</td>
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</tr>
<tr>
<td>PRODUCT DESCRIPTION</td>
<td>T400GD-8P-OSFP</td>
<td>T400GD-4P-OSFP</td>
<td>T400GDR-8P-OSFP</td>
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<td>----------------------------------------------------</td>
</tr>
<tr>
<td><strong>Trackable Receive Flows per Port without Sequence Checking and with Tx/Rx Synch</strong></td>
<td>• 400GE: 32K full statistics</td>
<td>• 2x200GE: 32K full statistics</td>
<td>• 400GE: 32K full statistics</td>
</tr>
<tr>
<td></td>
<td>• 4x100GE: 4K full statistics and 32K with minimum statistics</td>
<td>• 4x100GE: 4K full statistics and 32K with minimum statistics</td>
<td>• 4x100GE: 4K full statistics and 32K with minimum statistics</td>
</tr>
<tr>
<td></td>
<td>• 8x50GE: 4K full statistics and 16K with minimum statistic</td>
<td>• 8x50GE: 4K full statistics and 16K with minimum statistic</td>
<td>• 8x50GE: 4K full statistics and 16K with minimum statistic</td>
</tr>
<tr>
<td><strong>Trackable Receive Flows per Port with and without Sequence checking and no Tx/RX Synch</strong></td>
<td>• 400GE: 32K full statistics</td>
<td>• 2x200GE: 32K full statistics</td>
<td>• 400GE: 8K full statistics and 32K with minimum statistics</td>
</tr>
<tr>
<td></td>
<td>• 4x100GE: 8K full statistics and 32K with minimum statistics</td>
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<td></td>
<td>• 8x50GE: 8K full statistics and 16K with minimum statistic</td>
<td>• 8x50GE: 8K full statistics and 16K with minimum statistic</td>
<td>• 8x50GE: 8K full statistics and 16K with minimum statistic</td>
</tr>
<tr>
<td><strong>Minimum Frame Size</strong></td>
<td>• 400GE and 2x200GE: 60 bytes</td>
<td>• 4x100GE and 8x50GE: 64 bytes</td>
<td>• 400GE and 2x200GE: 60 bytes</td>
</tr>
<tr>
<td><strong>Filters (User-Defined Statistics, UDS)</strong></td>
<td>2 SA/DA pattern matchers, 2x16-byte user-definable patterns. 6 UDS counters are available with offsets for start of frame.</td>
<td>2 SA/DA pattern matchers, 2x16-byte user-definable patterns. 6 UDS counters are available with offsets for start of frame.</td>
<td>2 SA/DA pattern matchers, 2x16-byte user-definable patterns. 6 UDS counters are available with offsets for start of frame.</td>
</tr>
<tr>
<td><strong>Hardware Capture Buffer</strong></td>
<td>• 400GE: 1MB</td>
<td>• 2x200GE, 4x100GE and 8x50GE: 1MB (3)</td>
<td>• 400GE: 1MB</td>
</tr>
<tr>
<td>(3) Note: There is a hardware capture buffer per a fan-out resource group that may be assigned to one port of the fanout resource group</td>
<td></td>
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</tr>
<tr>
<td><strong>Standard Statistics and Rates</strong></td>
<td>Link state, line speed, frames sent, valid frames received, bytes sent/received, fragments, undersize, oversize, CRC errors, 6 user-defined stats, capture trigger (UDS 3), capture filter (UDS 4), data integrity frames, data integrity errors, sequence checking frames, sequence checking errors, ARP, and PING requests and replies</td>
<td>Link state, line speed, frames sent, valid frames received, bytes sent/received, fragments, undersize, oversize, CRC errors, 6 user-defined stats, capture trigger (UDS 3), capture filter (UDS 4), data integrity frames, data integrity errors, sequence checking frames, sequence checking errors, ARP, and PING requests and replies</td>
<td>Link state, line speed, frames sent, valid frames received, bytes sent/received, fragments, undersize, oversize, CRC errors, 6 user-defined stats, capture trigger (UDS 3), capture filter (UDS 4), data integrity frames, data integrity errors, sequence checking frames, sequence checking errors, ARP, and PING requests and replies</td>
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<th>T400GD-4P-OSFP FULL FEATURE 8-PORT / 4-PORT</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>FEC Statistics</strong></td>
<td>400GE and 2x200GE:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• FEC port statistics: Total Bit Errors, Max Symbol Errors, Corrected Codewords, Total Codewords, Uncorrectable Codewords, Frame Loss Ratio, Pre-FEC Bit Error Rate, and Codeword error distribution analysis</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• FEC per lane Rx statistics: FEC Symbol Error Count, Corrected Bits Count, Symbol Error Rate, Corrected Bit Rate</td>
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<tr>
<td></td>
<td>4x100GE and 8x50GE: (4)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Corrected and uncorrectable codewords</td>
<td></td>
</tr>
<tr>
<td></td>
<td>(4) Note: This is a minimum specification; consult factory for more information</td>
<td></td>
</tr>
<tr>
<td><strong>Latency / Jitter Measurements</strong></td>
<td>Cut-through, store &amp; forward, forwarding delay, latency/jitter, MEF jitter, and inter-arrival time</td>
<td></td>
</tr>
<tr>
<td><strong>Receive-side PCS Lanes Port Statistics Counters</strong></td>
<td>PCS: Sync Errors, Illegal Codes, Remote Faults, Local Faults, Illegal Ordered Set, Illegal Idle, and Illegal SOF</td>
<td></td>
</tr>
<tr>
<td><strong>400GE Physical Coding Sublayer (PCS) Receive-Side Statistics and Indicators</strong></td>
<td>Per-lane PCS receive capabilities include:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Receive – per-lane PCS receive statistics; Physical Lane assignments, Lane Marker Lock, Lane Market Map, Relative Lane Skew, Lane Marker Error Count</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Receive – per-lane FEC receive statistics; FEC Symbol Error Count, FEC Corrected Bits Count, FEC Symbol Error Rate, FEC Corrected Bit Rate</td>
<td></td>
</tr>
<tr>
<td><strong>Layer 2-3 Protocol Support</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Basic</strong></td>
<td>IxNetwork Base, RFC2544/2889/3918 QuickTest</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Complete protocol coverage as shown on the left side of this row only with reduced session scale:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• 100 routing &amp; switching sessions</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• 2,000 host/access sessions</td>
<td></td>
</tr>
<tr>
<td>PRODUCT DESCRIPTION</td>
<td>T400GD-8P-OSFP</td>
<td>T400GD-4P-OSFP</td>
</tr>
<tr>
<td>---------------------</td>
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</tr>
</tbody>
</table>
| **Software Defined Network** | BGP4/BGP4+, OSPFv2/v3, ISISv4/v6, RIP/RIPng, BFD; EVPN, VXLAN, GENEVE, Segment Routing (MPLS and IPv6), BGP-LS, PCEP, BGP SR-TE Policy, BGP FlowSpec, OVSDB, Netconf, BIER, OpenFlow; GRE and Protocol over GRE, LACP/Protocol over LACP, eCPRI; REQUIRES: 930-2201 IxNetwork Basic package for AresONE | Complete protocol coverage as shown on the left side of this row only with reduced session scale:  
- 100 routing & switching sessions  
- 2,000 host/access sessions | | |
| **MPLS and VPN** | BGP4/BGP4+, OSPFv2/v3, ISISv4/v6, RIP/RIPng, EIGRP, BFD, RSVP-TE P2P/P2MP, LDP/LDPv6/mLDP, LDP L2VPN (PWE/VPLS), BGP VPLS/VPWS, L3VPN/6VPE, BGP RFC3107, PIM-SM/SSM, Multicast VPN, MPLS-TP, MPLS OAM, EVPN/PBB-EVPN; REQUIRES: 930-2201 IxNetwork Basic package for AresONE | Complete protocol coverage as shown on the left side of this row only with reduced session scale:  
- 100 routing & switching sessions  
- 2,000 host/access sessions | | |
| **Broadband Access and Authentication** | PPPoX/L2TPv2, DHCPv4/DHCPv6, ANCP, IGMP/MLD, IPv6 Autoconfiguration (SLAAC), 802.1x, Bonded GRE HG, GRE/Protocol over GRE, LACP/Protocol over LACP, Session Aware Traffic, Service over MPLS, Broadband Control Plane QT, Asymmetric Data Performance QT; REQUIRES: 930-2201 IxNetwork Basic package for AresONE | Complete protocol coverage as shown on the left side of this row only with reduced session scale:  
- 100 routing & switching sessions  
- 2,000 host/access sessions | | |
| **Data Center Ethernet** | BGP4/BGP4+, OSPFv2/v3, ISISv4/v6, RIP/RIPng, BFD; EVPN, VXLAN, GENEVE, OVSDB, DCBX, FCoE, Fabric Path, SPBM, VEPA, TRILL, FCoE QT, IxCloudPerf QT, RFC7747 BGP Convergence QT, LACP/Protocol over LACP; REQUIRES: 930-2201 IxNetwork Basic package for AresONE | Complete protocol coverage as shown on the left side of this row only with reduced session scale:  
- 100 routing & switching sessions  
- 2,000 host/access sessions | | |
APPLICATION SUPPORT

<table>
<thead>
<tr>
<th>OSFP-400GE / OSFP-R400GE</th>
</tr>
</thead>
<tbody>
<tr>
<td>• IxExplorer: Layer 1-3 wire-speed traffic generation, capture, and analysis with L1 BERT, Forward Error Correction, FEC distribution analysis, and error injection with statistics, PCS Lanes Tx/Rx error injection and testing with statistics and reporting capability.</td>
</tr>
<tr>
<td>• IxNetwork: Wire-rate traffic generation with service modeling that builds realistic, dynamically controllable data-plane traffic. IxNetwork offers the industry's best test solution for functional and performance testing by using comprehensive emulation for routing, switching, MPLS, IP multicast, broadband, authentication, Carrier Ethernet, and data center Ethernet protocols.</td>
</tr>
<tr>
<td>• IxSuiteStore: Test suite for functional validation of PCS lanes BER, KP4 FEC Bit-error distribution with error insertion and link stability based on IEEE 802.3bs specification (at 400GE speed only)</td>
</tr>
<tr>
<td>• Tcl API: Custom user script development for Layer 1-3 testing</td>
</tr>
</tbody>
</table>

ORDERING INFORMATION

**FIXED CHASSIS SYSTEMS**

**944-1174**

Ixia, AresONE T400GD-8P-OSFP, High Density, 8-port, full performance fixed chassis model with native OSFP 400GE physical interfaces and L1-3 support (944-1174). Includes installation of the latest production released version of the IxOS software. Includes 3 each 100-125VAC power cords for North American operation. NOTE: for 200-240VAC operation please order, at no charge, the AresONE 200-240VAC Power Cord Option Kit for AresONE QSFP-DD and OSFP fixed chassis models (942-0110).
DATA SHEET

944-1175
Ixia, AresONE T400GDR-8P-OSFP, High Density, 8-port, reduced performance fixed chassis model with native OSFP 400GE physical interfaces and L1-3 support (944-1175). Includes installation of the latest production released version of the IxOS software. Includes 3 each 100-125VAC power cords for North American operation. NOTE: for 200-240VAC operation please order, at no charge, the AresONE 200-240VAC Power Cord Option Kit for AresONE QSFP-DD and OSFP fixed chassis models (942-0110).

944-1176
Ixia, AresONE T400GD-4P-OSFP, High Density, 4-port, full performance fixed chassis model with native OSFP 400GE physical interfaces and L1-3 support (944-1176). Includes installation of the latest production released version of the IxOS software. Includes 3 each 100-125VAC power cords for North American operation. NOTE: for 200-240VAC operation please order, at no charge, the AresONE 200-240VAC Power Cord Option Kit for AresONE QSFP-DD and OSFP fixed chassis models (942-0110).

944-1177
Ixia, AresONE T400GDR-4P-OSFP, High Density, 4-port, reduced performance fixed chassis model with native OSFP 400GE physical interfaces and L1-3 support (944-1177). Includes installation of the latest production released version of the IxOS software. Includes 3 each 100-125VAC power cords for North American operation. NOTE: for 200-240VAC operation please order, at no charge, the AresONE 200-240VAC Power Cord Option Kit for AresONE QSFP-DD and OSFP fixed chassis models (942-0110).

FAN-OUT OPTIONS

905-1044
Ixia, AresONE T400GD/T400GDR Fan-out option: 2x200GE, 4x100GE, 8x50GE FAN-OUT FACTORY INSTALLED option for the QSFP-DD and OSFP T400GD/T400GDR 8-port and 4-port, full and reduced, fixed chassis systems. One option is required for each fixed chassis system for all 8x400GE physical ports. Note: This option is REQUIRED ON NEW PURCHASES to enable the 2x200GE, 4x100GE, 8x50GE fan-out speeds per port.

905-1045
Ixia, AresONE T400GD/T400GDR Fan-out option: 2x200GE, 4x100GE, 8x50GE FAN-OUT FIELD UPGRADE Option for the QSFP-DD and OSFP T400GD/T400GDR 8-port and 4-port, full and reduced, fixed chassis systems. One option is required for each fixed chassis system for all 8x400GE physical ports. Note1: This option is REQUIRED ON FIELD UPGRADE PURCHASES to enable the 2x200GE, 4x100GE, 8x50GE fan-out speeds. Note2: For the 2x200GE, 4x100GE, 8x50GE fan-out speed option upgrade purchase, please provide the serial number of the desired fixed chassis to install the option on at the time of order placement.
IXNETWORK ARESONE ONLY - SOFTWARE BUNDLE OPTIONS

930-2200
IxNetwork, All Inclusive package for AresONE. Supports all IxNetwork software features with exclusion;
Excludes: 930-3461 IxNetwork AppLibrary Slot Bundle, Layer 4-7 Performance Test Application; 930-
2207 IxNetwork Encryption test package for AresONE. Any optional script package or IxSuiteStore
optional test suite is not considered as part of IxNetwork software features.*

*Note: AresONE does not support traditional IxNetwork a la carte license, bundle license and tier
licenses.

930-2201
IxNetwork Basic package for AresONE; INCLUDES: IxNetwork Base, RFC2544/2889 QuickTest.

930-2202
IxNetwork Routing, Switching and Carrier Ethernet package for AresONE; Includes BGP4/BGP4+,
OSPFv2/v3, ISISv4/v6, RIP/RIPng, EIGRP, BFD, IGMP/MLD/PIM-SM/SSM, LACP/Protocol over
LACP, STP/RSTP/MSTP/PVST, GRE and Protocol over GRE, CFM/Y.1731, Link-OAM, PBB-TE, ELMI,
1588v2/SyncE ESMC, Y.1564QT, TWAMP, NTP, LISP; REQUIRES: 930-2201 IxNetwork Basic package
for AresONE.

930-2203
IxNetwork MPLS and VPN package for AresONE; INCLUDES: BGP4/BGP4+, OSPFv2/v3, ISISv4/v6,
RIP/RIPng, BFD, RSVP-TE/P2MP, LDP/mLDP/LDPv6, L3VPN/6VPE, NGmVPN, PIM-SM/SSM/mVPN,
MPLS-TP, MPLS OAM, GRE and Protocol over GRE, LACP/Protocol over LACP; REQUIRES: 930-2201
IxNetwork Basic package for AresONE.

930-2204
IxNetwork SDN package for AresONE; INCLUDES: BGP4/BGP4+, OSPFv2/v3, ISISv4/v6, RIP/RIPng,
BFD; EVPN, VXLAN, GENEVE, Segment Routing, BGP-LS, PCEP, BGP SR-TE Policy, BGP FlowSpec,
OVSDB, Netconf, BIER, OpenFlow; GRE and Protocol over GRE, LACP/Protocol over LACP; 
REQUIRES: 930-2201 IxNetwork Basic package for AresONE.

930-2205
IxNetwork Data Center package for AresONE; INCLUDES: BGP4/BGP4+, OSPFv2/v3, ISISv4/v6,
RIP/RIPng, BFD; EVVPN, VXLAN, GENEVE, OVSDB, D CBX, FCoE, Fabric Path, SPBM, V EPA, TRILL,
FCoE QT, IxCloudPerf QT, RFC7747 BGP Convergence QT, LACP/Protocol over LACP; REQUIRES:
930-2201 IxNetwork Basic package for AresONE.

930-2206
IxNetwork Broadband Access and Authentication package for AresONE; INCLUDES: PPPoX/L2TP,
DHCPv4/v6, ANCP, IGMP/MLD/IPTV, 802.1x, GRE/Protocol over GRE, LACP/Protocol over LACP,
Session Aware Traffic, Service over MPLS, Broadband Control Plane QT, Asymmetric Data Performance
QT; REQUIRES: 930-2201 IxNetwork Basic package for AresONE.

930-2207
IxNetwork, Encryption Test package for AresONE (930-2207); INCLUDES: MACsec Emulation;
REQUIRES: 930-2201 IxNetwork Basic package for AresONE; Recommend with: 930-3461 IxNetwork
AppLibrary Slot Bundle, Optional Software, Layer 4-7 Performance Test Application for additional
encryption/decryption capability in Static MACsec emulation
**IXSUITE STORE SOFTWARE OPTION**

930-6001

Ixia, IxSuiteStore optional test suite for functional validation of PCS lanes BER, KP4 FEC Bit-error distribution with error insertion and Link stability based on IEEE 802.3bs specification (at 400GE speed only). This software is compatible with the following hardware platforms with the native QSFP-DD 400GE interfaces: K400 QSFP-DD-400GE (944-1152), K400 QSFP-DD-R400GE (944-1153); and all AresONE QSFP-DD and OSFP models: T400GD-8P-QDD (944-1170), T400GDR-8P-QDD (944-1171), T400GD-4P-QDD (944-1172), T400GDR-4P-QDD (944-1173), T400GD-8P-OSFP (944-1174), T400GDR-8P-OSFP (944-1175), T400GD-4P-OSFP (944-1176), T400GDR-4P-OSFP (944-1177).

**FIXED CHASSIS SYSTEM UPGRADE OPTIONS**

905-1040

Ixia, AresONE UPG-T400GDR-to-T400GD FIELD UPGRADE for the reduced performance T400GDR-8P-OSFP (944-1175) fixed chassis to become the full performance T400GD-8P-OSFP (944-1174) fixed chassis. Note: At the time of order placement of the purchase of the upgrade, please provide the serial number of the desired T400GDR-8P-OSFP reduced model to install the upgrade.

905-1041

Ixia, AresONE UPG-T400GD-4P-to-T400GD-8P FIELD UPGRADE for the 4-port full performance T400GD-4P-OSFP (944-1176) fixed chassis to the full performance 8-port T400GD-8P-OSFP (944-1174) fixed chassis. Note: At the time of order placement of the purchase of the upgrade, please provide the serial number of the desired T400GD-4P-OSFP full performance model to install the upgrade.

905-1042

Ixia, AresONE UPG-T400GDR-4P-to-T400GDR-8P FIELD UPGRADE for the 4-port reduced performance T400GDR-4P-OSFP (944-1177) fixed chassis to the reduced performance 8-port 400GDR-8P-OSFP (944-1175) fixed chassis. Note: At the time of order placement of the purchase of the upgrade, please provide the serial number of the desired T400GDR-4P-OSFP reduced model to install the upgrade.

905-1043

Ixia, AresONE UPG-T400GDR-4P-to-T400GD-4P FIELD UPGRADE for the reduced performance T400GDR-4P-OSFP (944-1177) fixed chassis to become the full performance T400GD-4P-OSFP (944-1176) fixed chassis. Note: At the time of order placement of the purchase of the upgrade, please provide the serial number of the desired T400GDR-4P-OSFP reduced model to install the upgrade.

**CABLES & TRANSCEIVERS**

**OSFP-1M-CBL**

Ixia, OSFP-to-OSFP 400GE 400GBASE-R passive copper, Direct Attach Cable (DAC), point-to-point cable, 1-meter length. This copper DAC is compatible with all AresONE OSFP models: T400GD-8P-OSFP (944-1174), T400GDR-8P-OSFP (944-1175), T400GD-4P-OSFP (944-1176), and T400GDR-4P-OSFP (944-1177).
OSFP-2M-CBL
Ixia, OSFP-to-OSFP 400GE 400GBASE-R passive copper, Direct Attach Cable (DAC), point-to-point cable, 2-meter length. This copper DAC is compatible with all AresONE OSFP models: T400GD-8P-OSFP (944-1174), T400GDR-8P-OSFP (944-1175), T400GD-4P-OSFP (944-1176), and T400GDR-4P-OSFP (944-1177).

200-240VAC POWER CORD OPTION
942-0110
Ixia, AresONE 200-240VAC Power Cord Option Kit includes 2 each C13 to 6-20P, 8 feet in length, and 2 each C13 to L6-20P, 10 feet in length. Two cord types are provided that accommodate the most common 200-240VAC power receptacle types. Two of either cord type is required to power the AresONE fixed chassis. These power cords are compatible with all AresONE models: Ixia T400GD-8P-QDD (944-1170), T400GDR-8P-QDD (944-1171), T400GD-4P-QDD (944-1172), T400GDR-4P-QDD (944-1173), T400GD-8P-OSFP (944-1174), T400GDR-8P-OSFP (944-1175), T400GD-4P-OSFP (944-1176), and T400GDR-4P-OSFP (944-1177). The kit is optional and is sold at no charge. It is REQUIRED only when an AresONE fixed chassis must be connected to 200-240VAC single phase power sources. Note: Requires (2) power sources when running single phase 200-240VAC drawing 7 Amps for each power supply.

\[\text{Note: With AN and LT turned on 50GE fan-out operation becomes 4x50GE per port. With AN and LT turned OFF the 50GE operation is 8x50GE per port}\]

Learn more at: www.ixiacom.com

For more information on Ixia products, applications, or services, please contact your local Ixia or Keysight Technologies office.
The complete list is available at: www.ixiacom.com/contact/info

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